

Some Application Notes on Reduction Operators

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1 Introduction

In earlier publications [3, 4] the theoretical background of reduction operators has been thoroughly discussed. This letter presents an interesting case study of the reduction operator approach to circuit equations. Based on an extremely simple example circuit, the relation between the nodal equations and the state variable equations is examined.

2 A review of the reduction operator

Let R_n be the reduction operator of rank n . Its formal definitions including all properties and proofs can be found in [3, 4]. For convenience though, we will repeat its most essential definition

$$R_n(A) \triangleq \begin{bmatrix} I_{n \times n} & 0 \\ -A_{21}A_{11}^{-1} & I \end{bmatrix}_{r \times r} \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix}_{r \times c} = \begin{bmatrix} A_{11} & A_{12} \\ 0 & \tilde{A}_{22} \end{bmatrix}_{r \times c} \triangleq \tilde{A}_U, \quad (1)$$

where

$$\tilde{A}_{22} \triangleq A_{22} - A_{21}A_{11}^{-1}A_{12}. \quad (2)$$

As can be seen from (1), the reduction operator of rank n reduces all elements in the $(r - n) \times n$ south–western corner of A to zero, transforming it to an upper block triangular form \tilde{A}_U [1]. A may be any matrix with more than n rows and columns, provided that there is a non-singular square submatrix A_{11} of order n in its north–western corner. The application of the reduction operator R_n actually implies the inversion of the north–western portion in A . Complementary to the reduction operator, we further define the *transposed* reduction operator

$$\begin{aligned} R_n^T(A) &\triangleq (R_n(A^T))^T = \left(\begin{bmatrix} I & 0 \\ -A_{12}^T A_{11}^{-T} & I \end{bmatrix} \begin{bmatrix} A_{11}^T & A_{21}^T \\ A_{12}^T & A_{22}^T \end{bmatrix} \right)^T = \\ &= \begin{bmatrix} A_{11} & A_{12} \\ A_{21} & A_{22} \end{bmatrix} \begin{bmatrix} I & -A_{11}^{-1}A_{12} \\ 0 & I \end{bmatrix} = \begin{bmatrix} A_{11} & 0 \\ A_{21} & \tilde{A}_{22} \end{bmatrix} \triangleq \tilde{A}_L, \end{aligned} \quad (3)$$

where \tilde{A}_{22} is identically equal to (2). As expected, the transposed reduction operator transforms matrix A into a lower block triangular form \tilde{A}_L [1].

R_m and its close relative R_n^T are then used together with three general permutation matrices P , Q and R to constitute a general operator equation, mapping any matrix equation

$$AB = 0 \quad (4)$$

into

$$R_m(PAQ^T) R_n^T(QBR^T) \triangleq R_m(\hat{A}) R_n^T(\hat{B}) \triangleq \tilde{\hat{A}}\tilde{\hat{B}} = 0. \quad (5)$$

It has been established [3, 4], that the repeating application of this operator equation suffices to describe entire circuit analysis approaches. For instance, a series of reduction operators can be used to execute a Gaussian forward elimination, a back substitution, a triangular decomposition or a Gauss–Jordan elimination. Also, a circuit’s nodal, cutset or loop equation set may be formulated solely by using reduction operators and permutation matrices.

3 The application example

Consider the following extremely simple two–node ($n = 2$) circuit in figure 1. It is composed of four branches ($b = 4$), namely one capacitor branch ($c = 1$), three resistive branches ($r = 3$) and no inductive branch ($l = 0$).

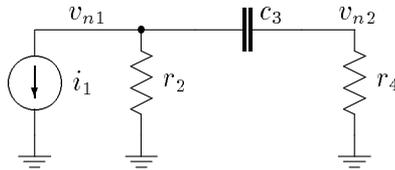


Figure 1: The sample circuit

This circuit requires a basic tableau (6) of 10th order [2]. Since we intend to apply reduction operators to the tableau, we use a rather unusual notation (6). The equation right hand side has been appended to the coefficient matrix on the left hand side, leaving a zero matrix behind. This notation also necessitates an additional -1 constant element in the variable vector. The blank areas in (6) correspond to zero matrices and should be considered full of zero coefficients.

The tableau in (6) is now suitable for reduction operators, since it has the required form (4). The matrix in (6), containing derivative operators, actually makes (6) a linear differential equation set rather than a linear one. In order to produce the correct nodal equations, we must replace all reactances in the circuit by adequate linear time invariant models. In our case, the capacitor branch equation $\frac{d}{dt}v_{b3} - i_{b3} = 0$ becomes $g_3v_{b3} - i_{b3} = i_3$. The current excitation $i_1(t)$ should be also considered time independent i_1 .

In our second order equation set the Gaussian elimination's forward course consists of a single reduction operator i.e. R_9 in (7). In this way only the last row of (9) is affected

$$\begin{bmatrix} \vdots & \vdots & \vdots \\ \dots & \frac{1}{r_2} + g_3 & -g_3 & i_3 - i_1 \\ \dots & 0 & \frac{1+r_4g_3}{r_4} - \frac{r_2g_3^2}{1+r_2g_3} & \frac{r_2g_3(i_3-i_1)}{1+r_2g_3} - i_3 \end{bmatrix} \begin{bmatrix} \vdots \\ v_{n1} \\ v_{n2} \\ -1 \end{bmatrix} = 0. \quad (10)$$

A second order back substitution should produce the circuit nodal voltages from the last two rows in (10). Considering the upper triangular matrix portion above the nodal equations however, we can run an extended 10th order back substitution, assembling the values of all circuit variables.

Using reduction operators [4], any back substitution is formally executed as a forward substitution of the reversed matrix order. The reversing matrices in (7) are antidiagonal unit matrices P_2 and Q_2 . The substitution itself needs only the two reduction operators R_1 and R_2 , in order to arrive at the final diagonal form, from which the solution vector is obtained by division.

Let us now go back to the circuit tableau (6) and use the state variable approach. This time the derivation operators are kept in the equation set rather than being replaced with time invariant models. The reduction operator expression for state variable analysis yields a remarkable resemblance between the nodal equations (7) and the state equations

$$\underbrace{R_4(R_1(P_2 R_9(R_6(R_3(P_1 \underbrace{\begin{bmatrix} A^T & -I & 0 & 0 \\ 0 & 0 & A & 0 \\ 0 & Y(t) & Z(t) & e(t) \end{bmatrix}}_{\text{Tableau (6)}} Q_1^T))) Q_2^T))}_{\text{State equations (13)}} (Q_2 Q_1 \begin{bmatrix} v_n \\ v_b \\ i_b \\ -1 \end{bmatrix}) = 0. \quad (11)$$

Output equations

In order to clarify (11) we will explicitly discuss its three stages. First, a permutation is needed to determine the circuits order of complexity and to select the state variables. This is done by heuristic methods and leads to the permutation matrices

$$P_1 = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}, \quad Q_1 = \begin{bmatrix} 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}. \quad (12)$$

Secondly, three reduction operators R_3 , R_6 and R_9 are applied in (11). The permuted equation set is transformed to an upper block triangular form with the state equations in the

References

- [1] R. P. Tewarson, *Sparse Matrices*, Academic Press, New York, NY, 1973.
- [2] L. O. Chua, P. M. Lin, *Computer-Aided Analysis of Electronic Circuits*, Prentice-Hall, Englewood Cliffs, NJ, 1975.
- [3] T. Tuma, F. Bratkovič, 'A Mathematical Model for Network Analysis Methods', *Proc. Eur. Conf. on Circuit Theory and Design-93*, Davos, 1993, pp. 397–402.
- [4] T. Tuma, F. Bratkovič, 'A General Approach to Circuit Equations', *The International Journal of Circuit Theory and Applications*, vol. 22, no. 6, pp. 431–445 (1994).